

FIGURE 1 (PRIOR ART)

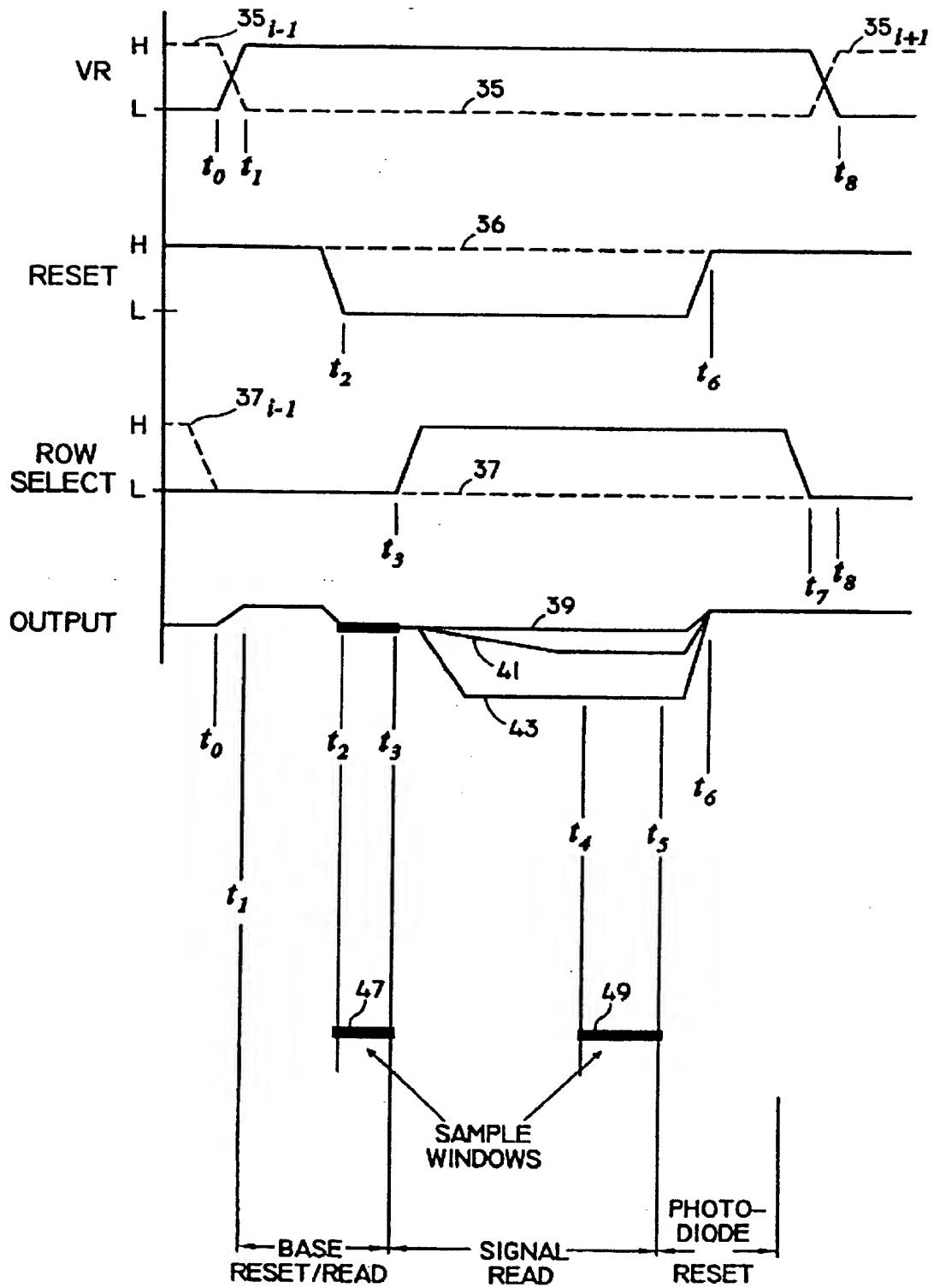


FIGURE 2 (PRIOR ART)

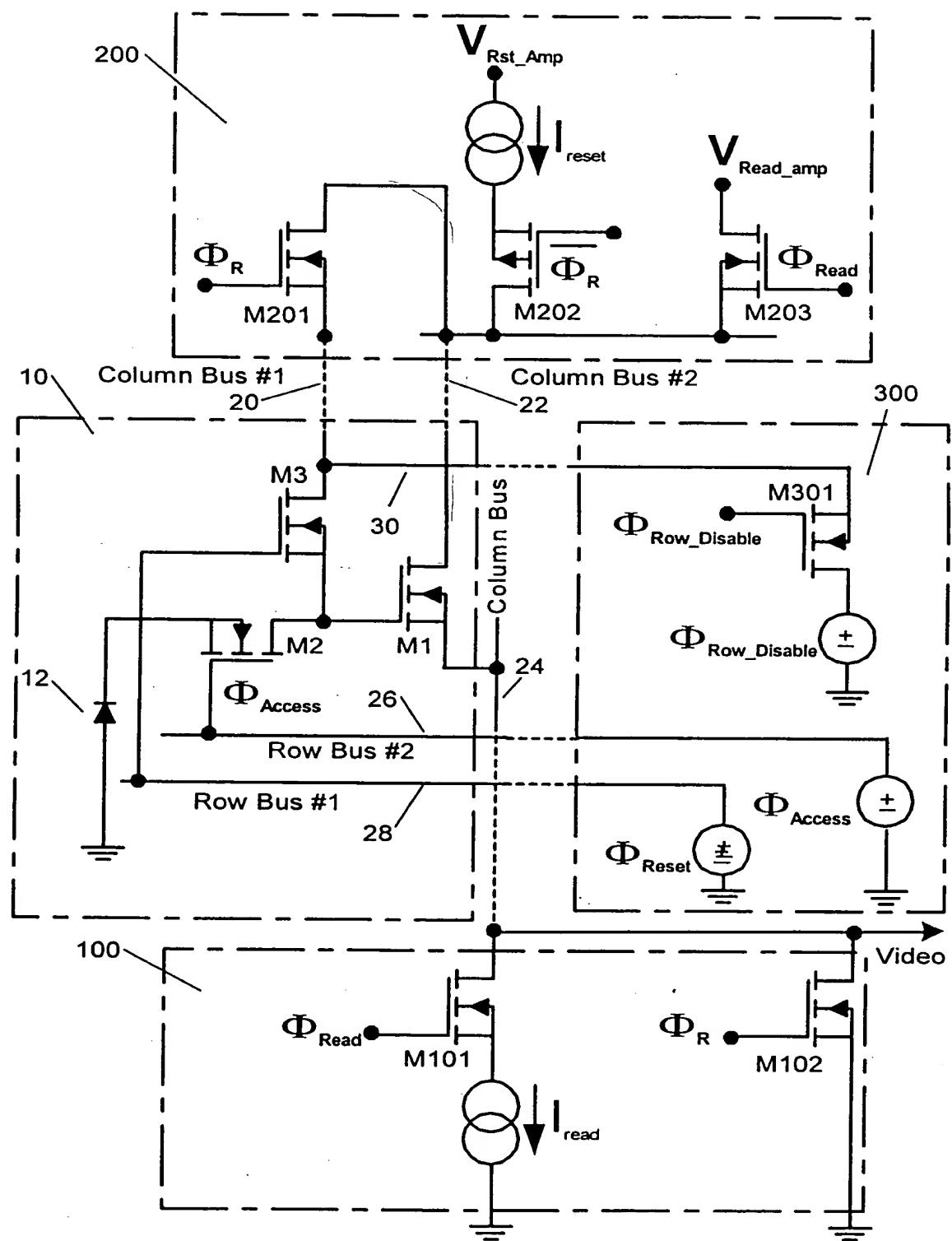


FIGURE 3

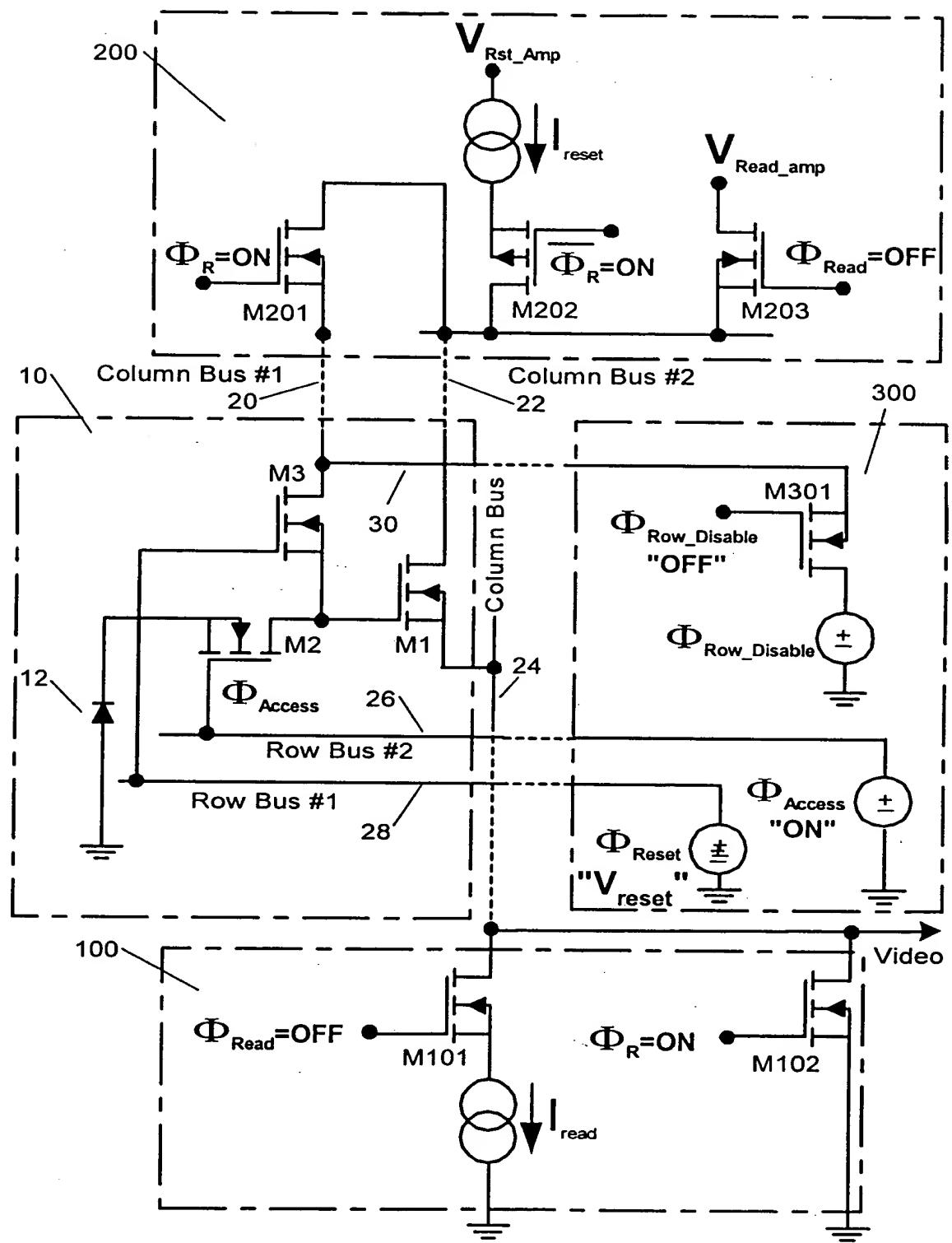


FIGURE 4 (RESET)

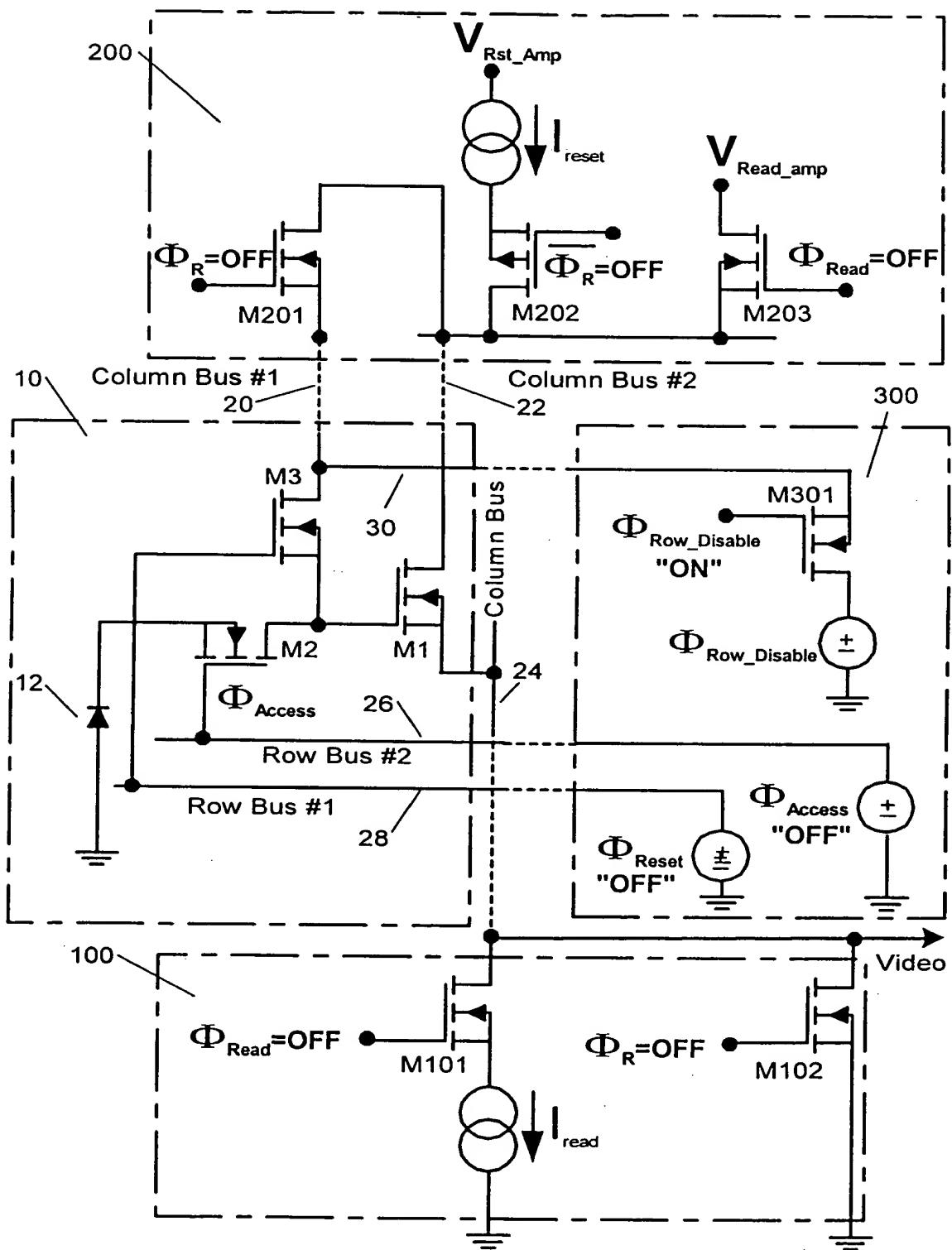


FIGURE 5 (INTEGRATE)

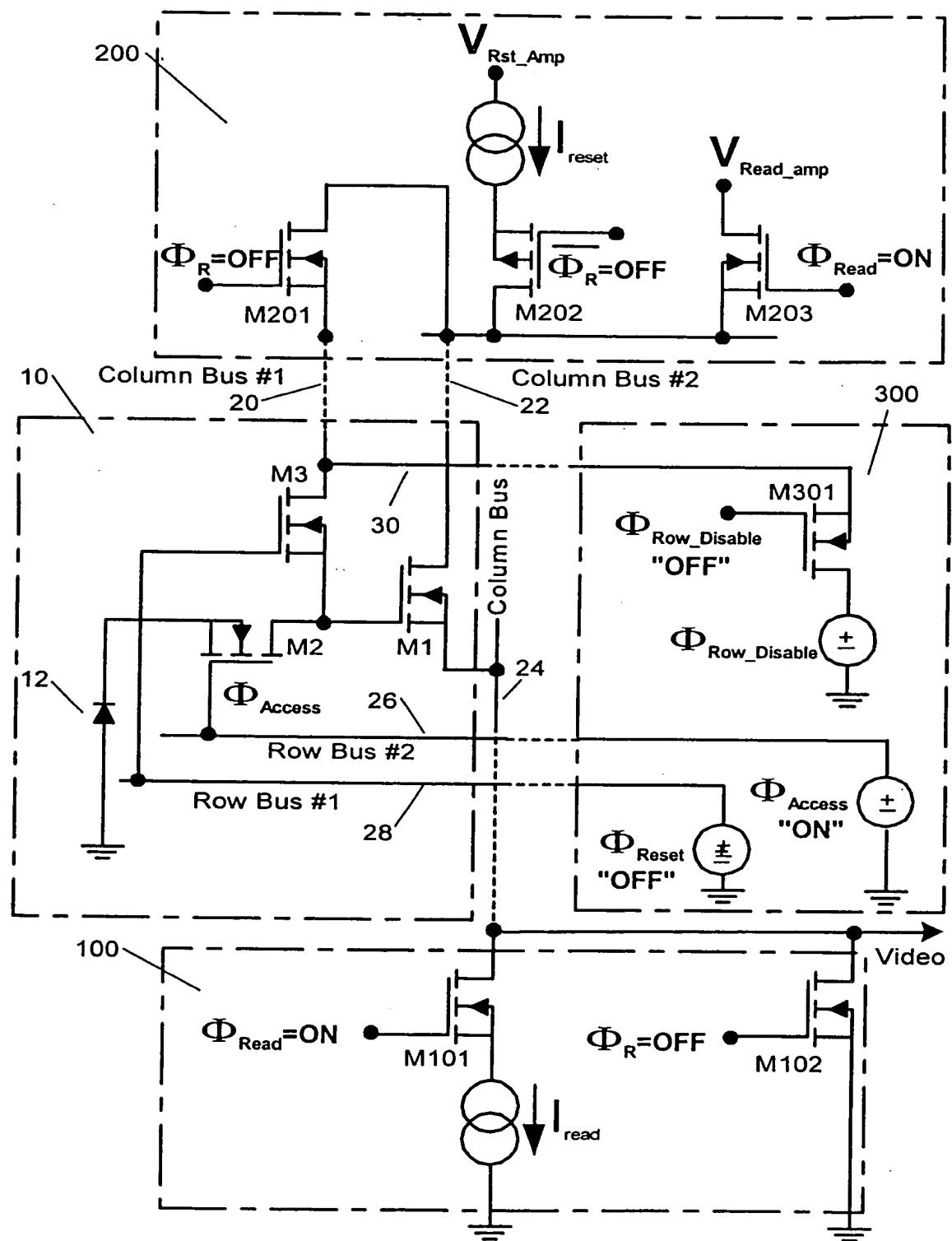


FIGURE 6 (READ)

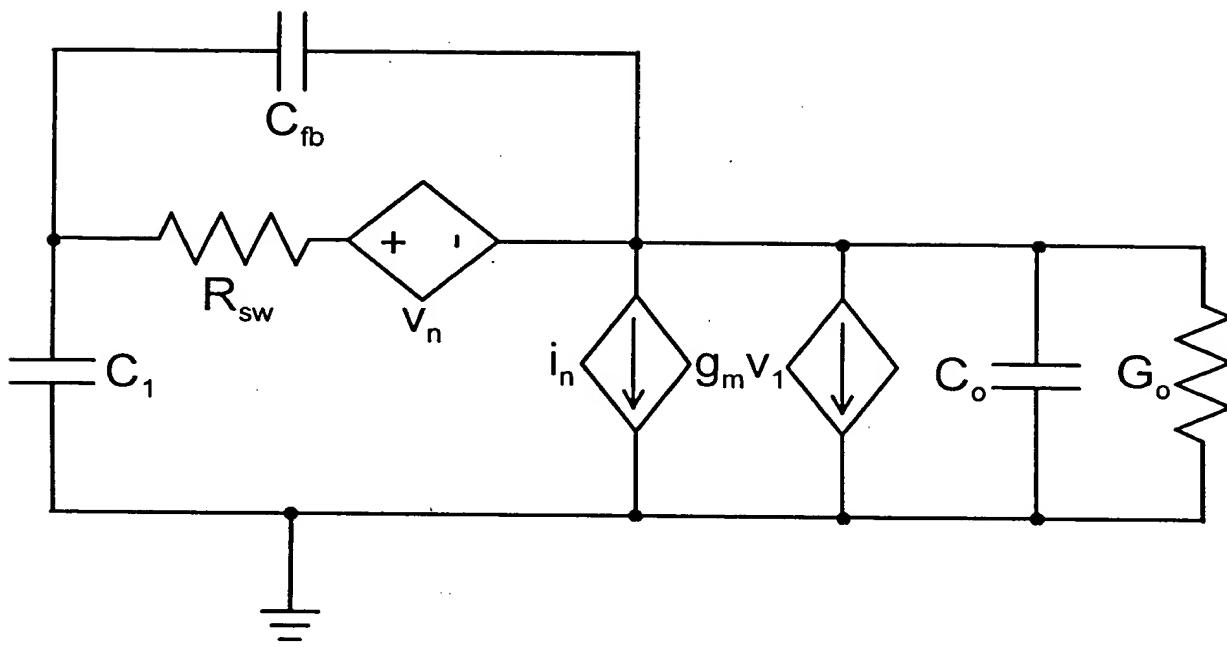


FIGURE 7

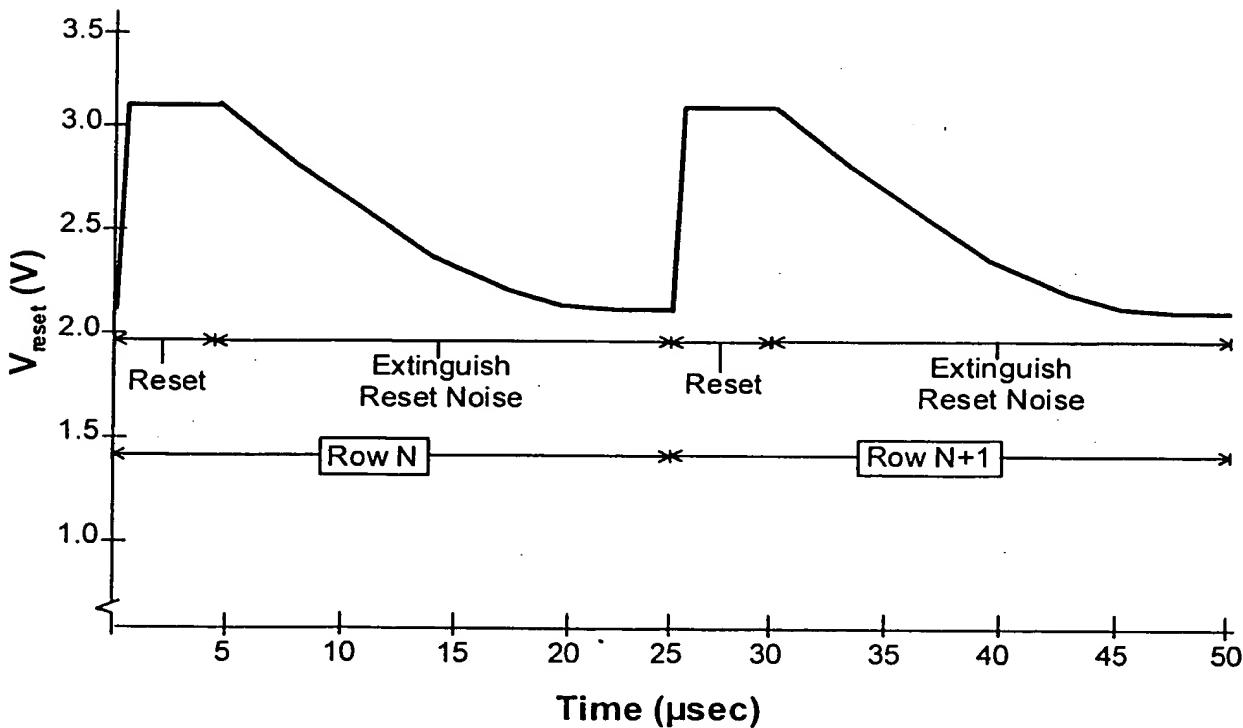


FIGURE 8

Representative Timing Diagram for X by Y CMOS Imager

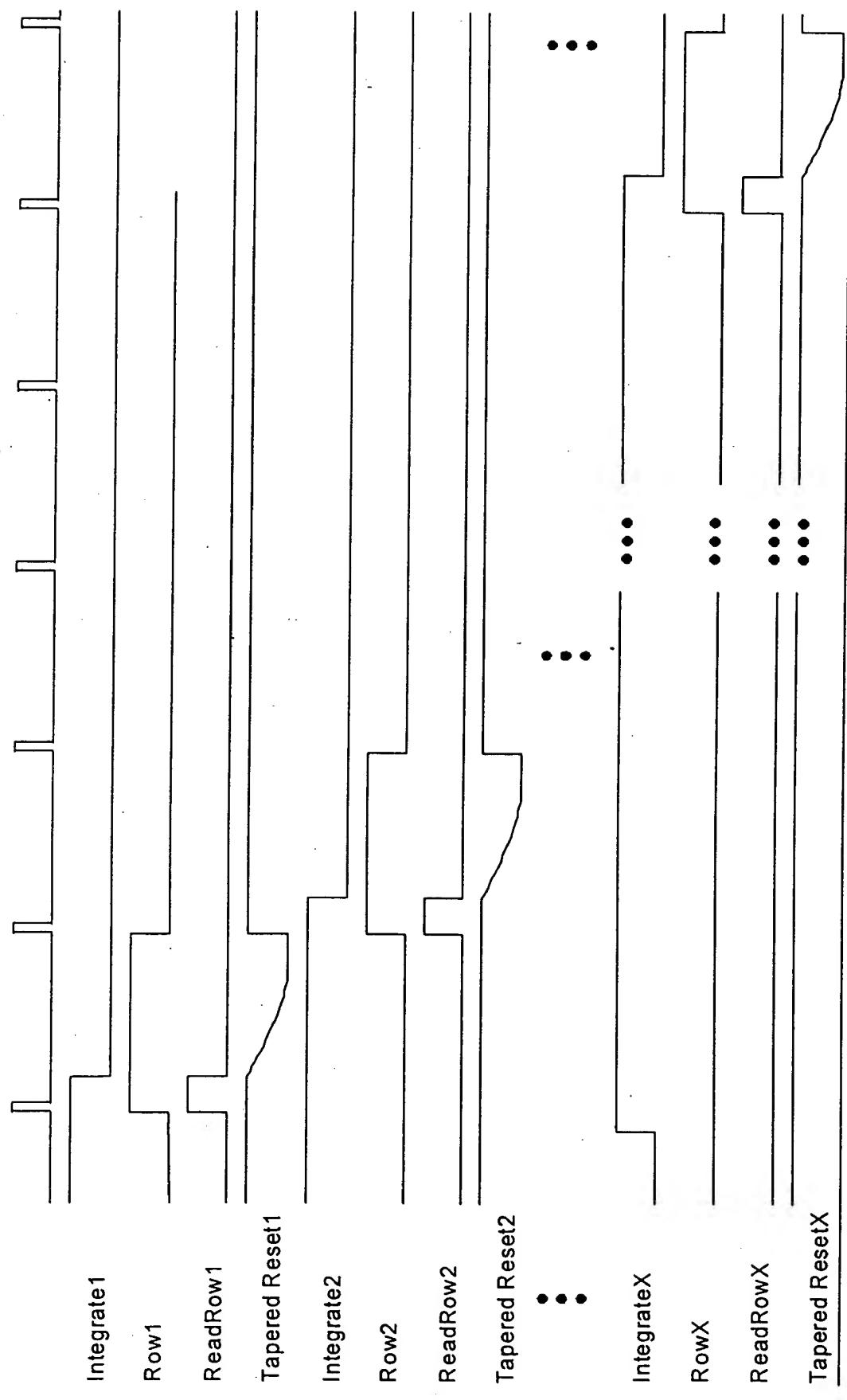


FIGURE 9